Docket No.

220327US2

Herewith

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Yasuhiko MATSUNAGA, et al.

SERIAL NO:

NEW APPLICATION

GAU:

EXAMINER:

FILED:

FOR:

NON-VOLATILE SEMICONDUCTOR MEMORY DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

ASSISTANT COMMISSIONER FOR PATENTS WASHINGTON, D.C. 20231

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Marvin J. Spivak

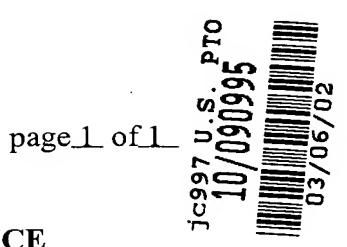
Registration No.

24,913

C. Irvin McClelland Registration Number 21,124

22850

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STATEMENT OF RELEVANCY

Reference AO (10-283788) on Form PTO-1449:

This reference shows similar gate bias voltages during programing (Fig. 4 and lines 92-100 of page 7).

Reference AP (8-279297) on Form PTO-1449:

This reference shows the original local self-boosting program method.

5 DTO 4440		U.S. DEPARTMENT O	E COMMERCE	ATTY DOCKET NO. 220327US2		SERIAL NO. NEW APPLICATION	
Form PTO 1449 (Modified)		PATENT AND TRADE	MARK OFFICE				
•				APPLICANT			
LIST OF	REFER	RENCES CITED BY APP	LICANT	Yasuhiko MATSUNAGA, et al.			
			FILING DATE		GROUP		
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
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	DOCUMENT DATE					TRANSLATION	
		NUMBER	DATE	COUNTRY		YES	NO
	AO	10-283788	10/23/98	Japan (with English abstract)			X
	AP	8-279297	10/22/96	Japan (with English abstract)			X
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		OTHER RE	FERENCES	(Including Author, Title, Date, Pertine	nent Pages, e	etc.)	
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Examiner		<u></u>			Date Considered		
*Examiner: I	nitial if	reference is considered, ot considered. Include co	whether or no	ot citation is in conformance with MPE m with next communication to applica	P 609; Draw nt.	line through	citation if not in